

Abstract of the Disclosure

A tuner architecture is disclosed that mixes an analog RF input signal and a digital local oscillator signal to generate a output signal at a desired IF frequency, including low-IF and zero-IF solutions. The tuner provides a number of advantages over previous implementations, such as improved performance for low-IF and zero-IF architectures and a significant reduction in interference between adjacent paths in a multiple tuner solution. Other features and variations can be implemented, if desired, and related methods can be utilized, as well.